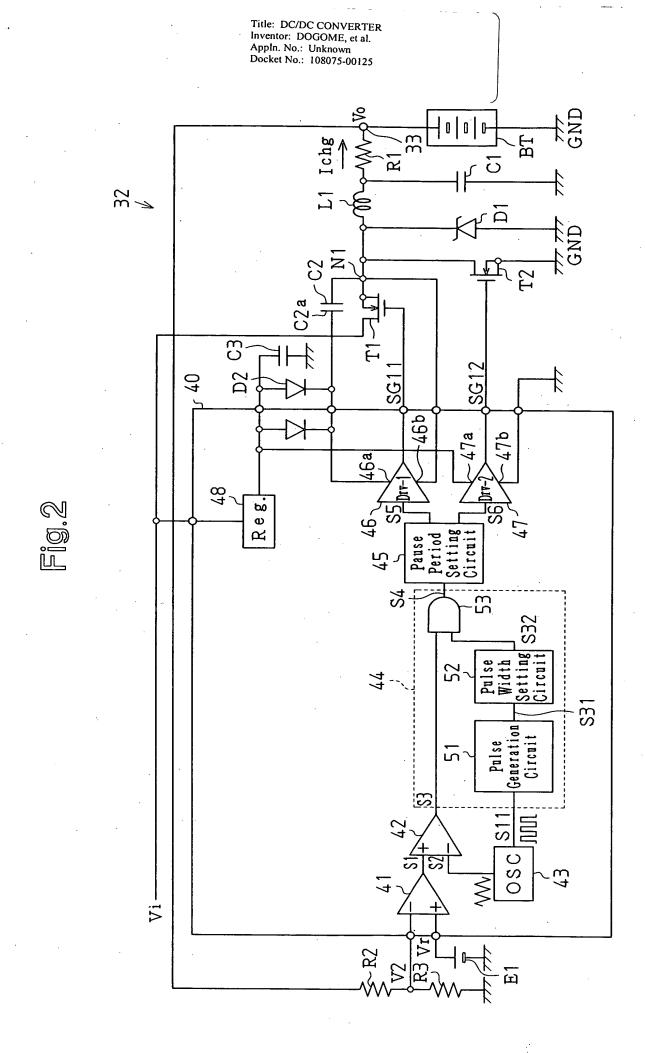
Title: DC/DC CONVERTER Inventor: DOGOME, et al. Appln. No.: Unknown Docket No.: 108075-00125

D2 SG1 ₽0 Re ထ္ Pause Period Setting Circuit OSC

Fig. 1 (Prior Art)



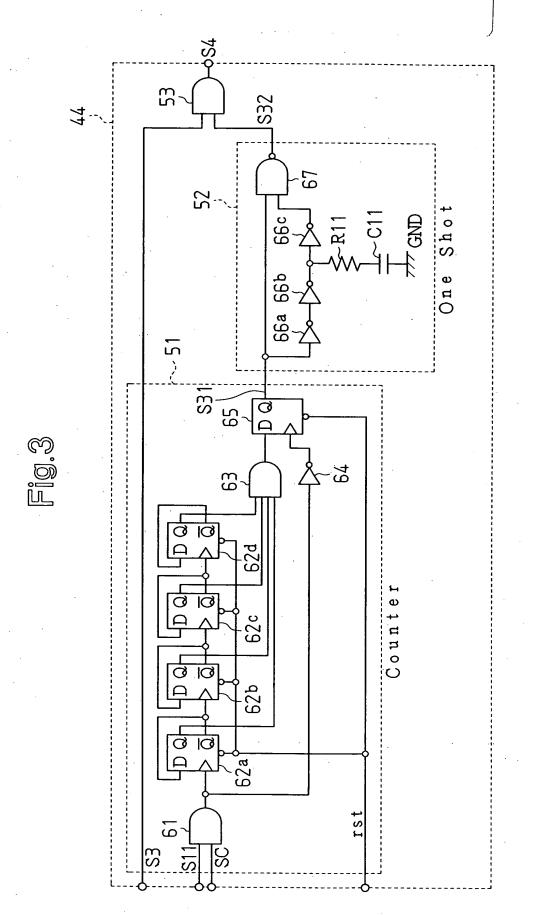


Fig.4

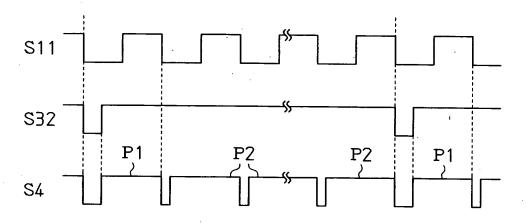
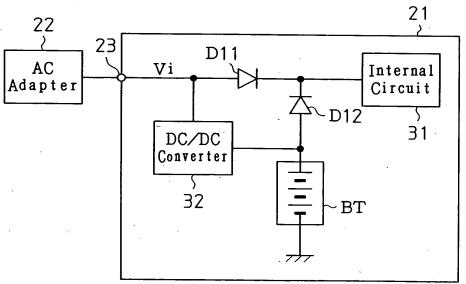
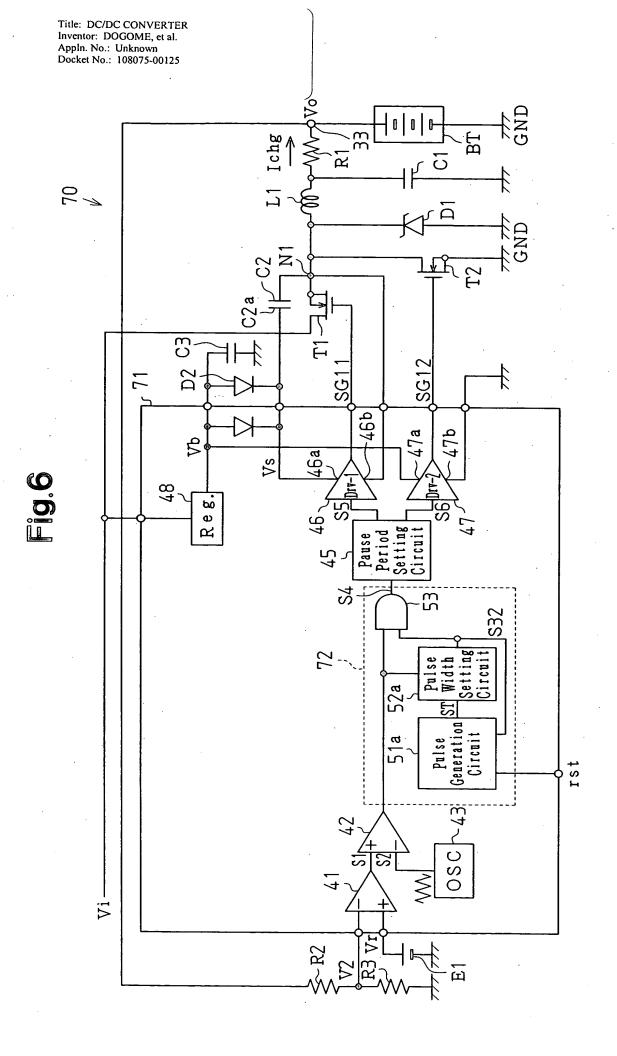


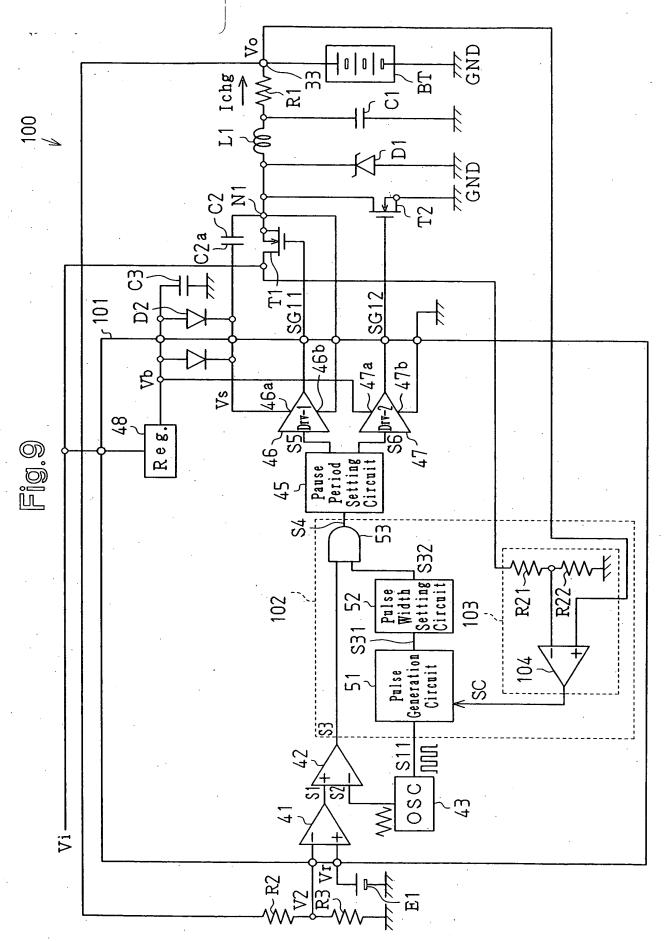
Fig.5





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Title: DC/DC CONVERTER Inventor: DOGOME, et al. Appln. No.: Unknown Docket No.: 108075-00125 0 0 L1 Ichg 모 06 / C2 C2a C3 D2 SG1 9 46b 46a Vs 48 46 S5 bó ക Pause Period Setting Circuit 召 \$4 pES Pulse Width Setting Circuit **S31** Delay Pulse Generation-Circuit 5 S11 \circ 080 $\nabla_{\mathbf{i}}$



Title: DC/DC CONVERTER Inventor: DOGOME, et al. Appln. No.: Unknown Docket No.: 108075-00125 모 N **D**2 \mathbb{S} 46a 2 ۷s **D**0 Φ 46~ \$12 召 48 **S**5 **S**8 Pause Period Setting Circuit S11 SC

Fig.11A

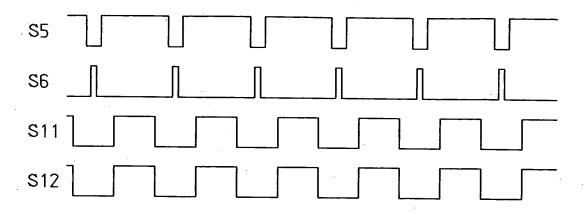
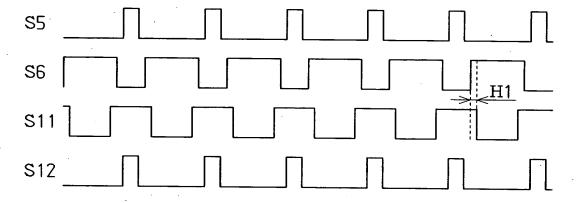


Fig.11B



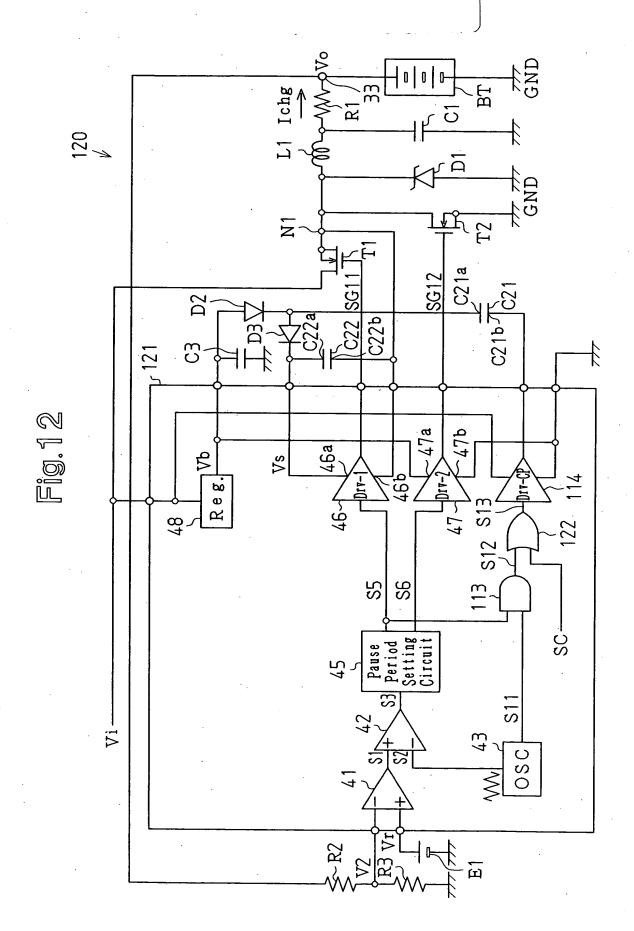


Fig.13

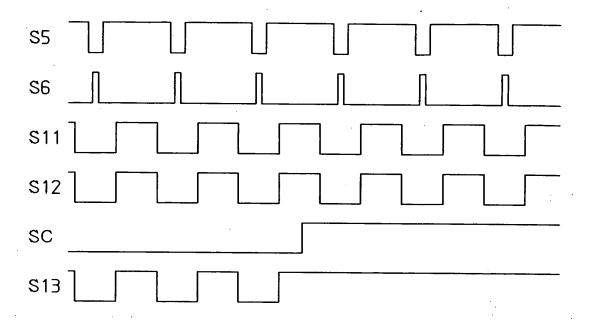


Fig.14

